ABSTRACT OF THE DISCLOSURE

An improved method of monitoring the output power provided by a switch mode power converter that reduces the overall cost of the converter. The method includes initiating a soft-start procedure for a first output voltage in the event the first voltage channel is enabled. When the first voltage level comes within regulation, a delay counter counts a predetermined number of clock cycles. In the event a second output voltage channel is enabled between the time the first channel is enabled and the time the first voltage comes within regulation, a soft-start procedure is initiated for the second voltage and the delay counter re-starts when the second voltage comes within regulation. After the delay counter finishes counting, the first and second voltages are considered stable and a single Power-Good signal is asserted. In the event one of the channels is disabled by a user while the other channel remains enabled, the disabled channel is ignored and the PGOOD signal stays asserted to indicate that the power provided by the converter on the enabled channel is good.

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